

Electronic Acknowledgement Receipt

EFS ID:	5145734
Application Number:	10575758
International Application Number:	
Confirmation Number:	5126
Title of Invention:	Printed Circuit Board Design Instruction Support Method and Device
First Named Inventor/Applicant Name:	Hiromichi Inaishi
Customer Number:	02292
Filer:	Paul C. Lewis/Candice Lee
Filer Authorized By:	Paul C. Lewis
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		20090413Amendment.PDF	1160504 <div>6a2468f5e81e630f4f232ad74e30feede73015d2</div>	yes	22
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Fee Worksheet (PTO-06)		1	1	
	Extension of Time		2	2	
	Amendment After Final		3	3	
	Claims		4	8	
	Applicant Arguments/Remarks Made in an Amendment		9	16	
	Claims		17	22	
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	30046 <div>2f125a22e6d4866bdd5811b8eee8529afdbad427</div>	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			1190550		

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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